

# An Overview of Quantifier Instantiation in Modern SMT Solvers

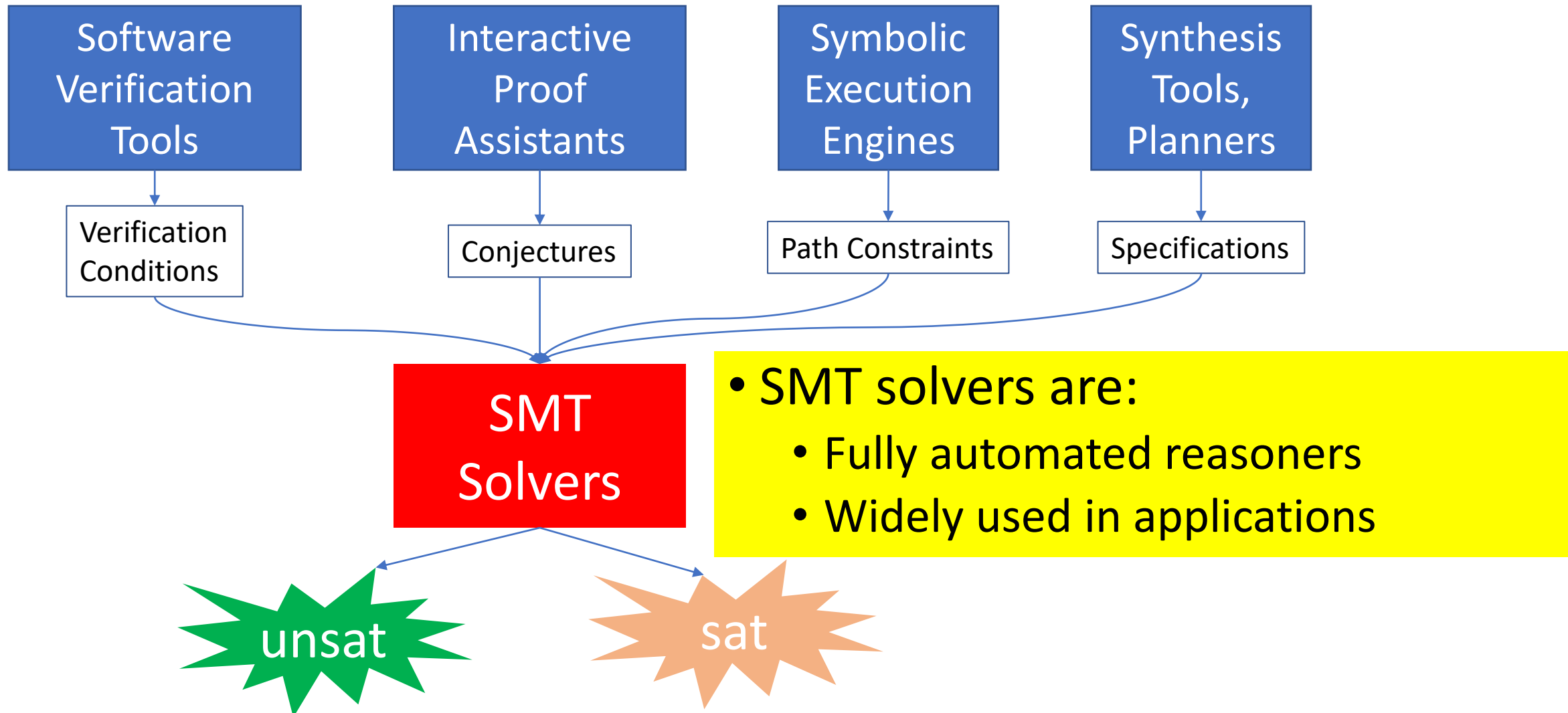
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May 28, 2021



THE UNIVERSITY  
OF IOWA

# Satisfiability Modulo Theories (SMT) Solvers



# SMT Solvers

- Traditionally:
  - Efficient decision procedures for *quantifier-free* constraints over theories:
    - Arithmetic
    - Uninterpreted functions (UF)
    - Bitvectors
    - Arrays
    - Datatypes
    - More recently: strings, floating points, sets, relations, ...
- In the past decade:
  - Efficient (heuristic) techniques for *quantified* formulas as well
    - ⇒ Focus of this talk

# Applications of $\forall$ in SMT

- Are used for:

- **Automated theorem proving:**

- Background axioms  $\{\forall x. g(e, x) = g(x, e) = x, \forall x. g(x, g(y, z)) = g(g(x, y), z), \forall x. g(x, i(x)) = e\}$

- **Software verification:**

- Unfolding  $\forall x. foo(x) = bar(x+1)$ , code contracts  $\forall x. pre(x) \Rightarrow post(f(x))$
    - Frame axioms  $\forall x. x \neq t \Rightarrow A'(x) = A(x)$

- **Function Synthesis:**

- Synthesis conjectures  $\forall i: input. \exists o: output. R[o, i]$

- **Planning:**

- Specifications  $\exists p: plan. \forall t: time. F[P, t]$

# SMT Solvers for $\forall$ using Quantifier Instantiation

- Traditionally:

- E-matching [Detslefs et al 2005, Bjorner et al 2007, Ge et al 2007]

Implemented in

simplify, z3, FX7,  
Alt-Ergo, Princess,  
cvc5, veriT, SMTInterpol

# SMT Solvers for $\forall$ using Quantifier Instantiation

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- E-matching [\[Detlefs et al 2005, Bjorner et al 2007, Ge et al 2007\]](#)

Implemented in

simplify, z3, FX7,  
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cvc5, veriT, SMTInterpol

- More recently:

- Conflict-Based Instantiation [\[Reynolds et al 2014, Barbosa et al 2017\]](#)
- Model-Based Instantiation [\[Ge et al 2009, Reynolds et al 2013\]](#)
- Enumerative Instantiation [\[Reynolds et al 2018\]](#)
- Counterexample-Guided / QE [\[Reynolds et al 2015, Janota et al 2015\]](#)
- Syntax-Guided [\[Preiner et al 2017, Niemetz et al 2021\]](#)

cvc5, veriT, SMTInterpol

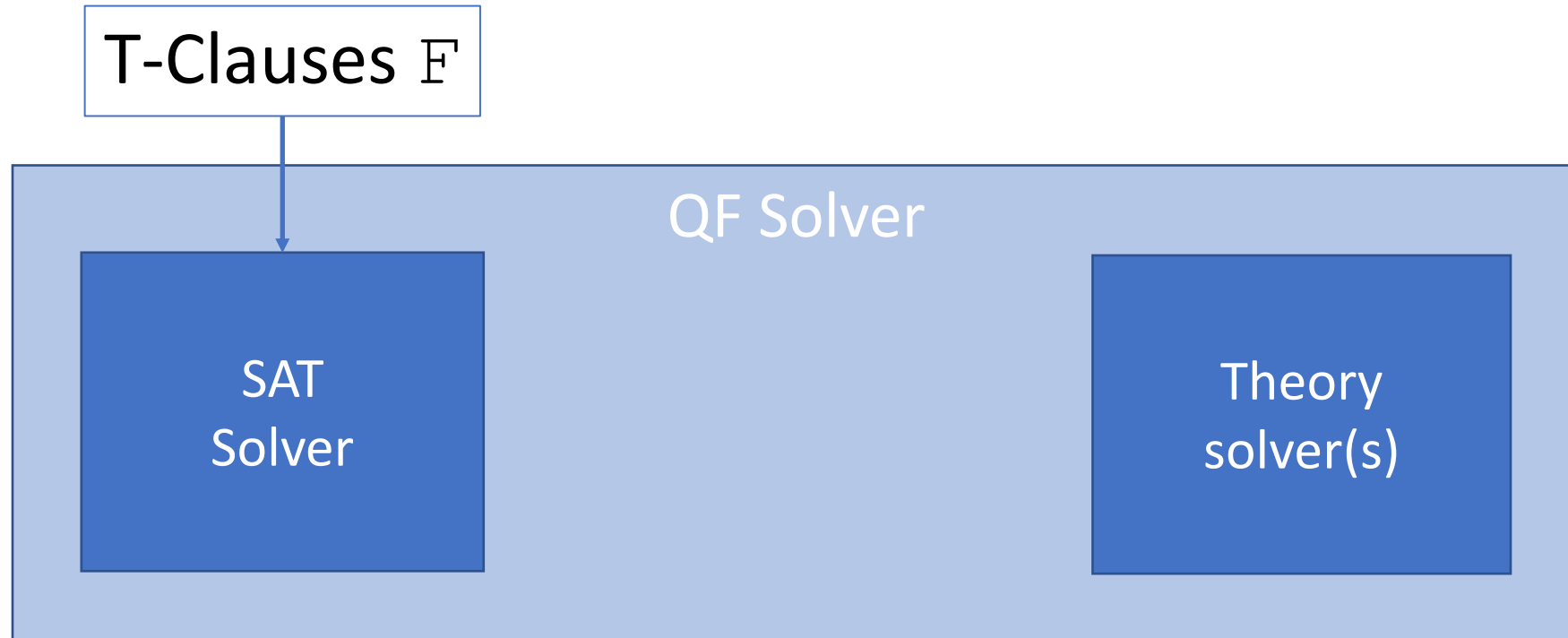
z3, cvc5

cvc5, veriT

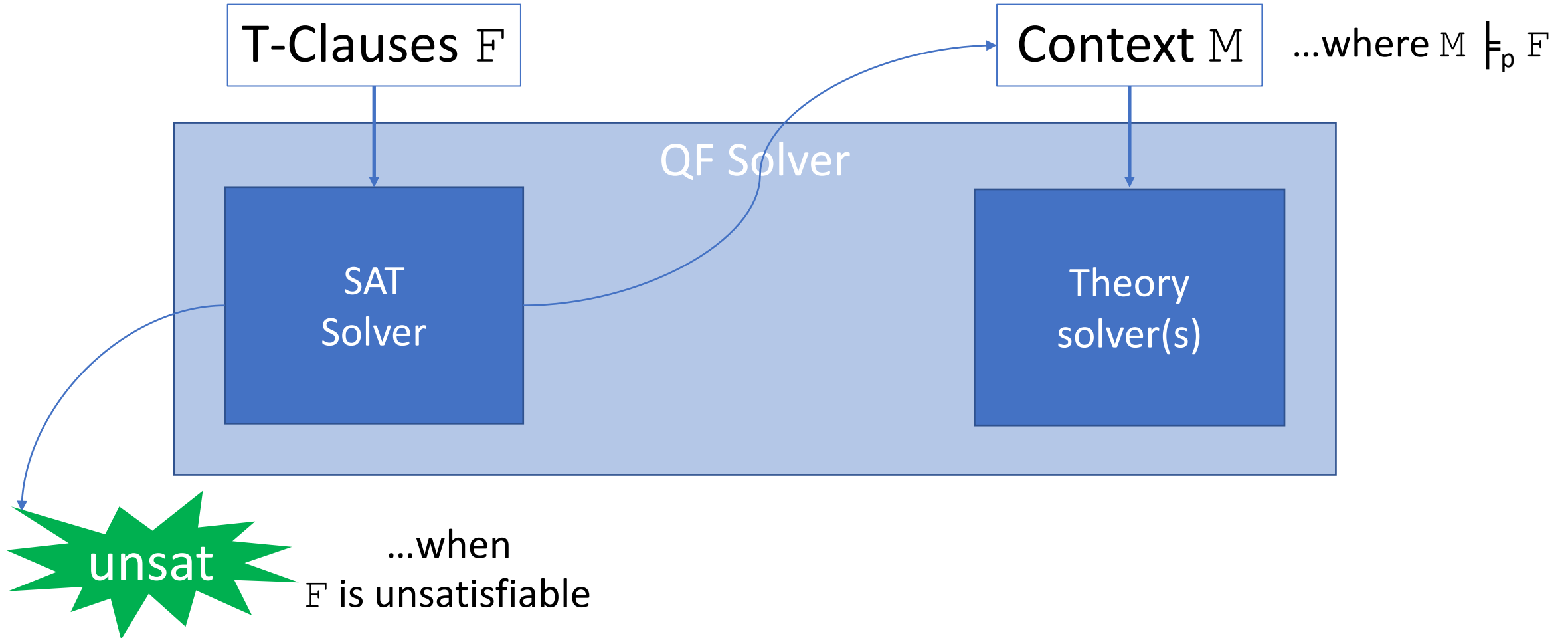
z3, cvc5, yices

boolector, cvc5

# DPLL(T)-Based SMT Solvers (quantifier-free)

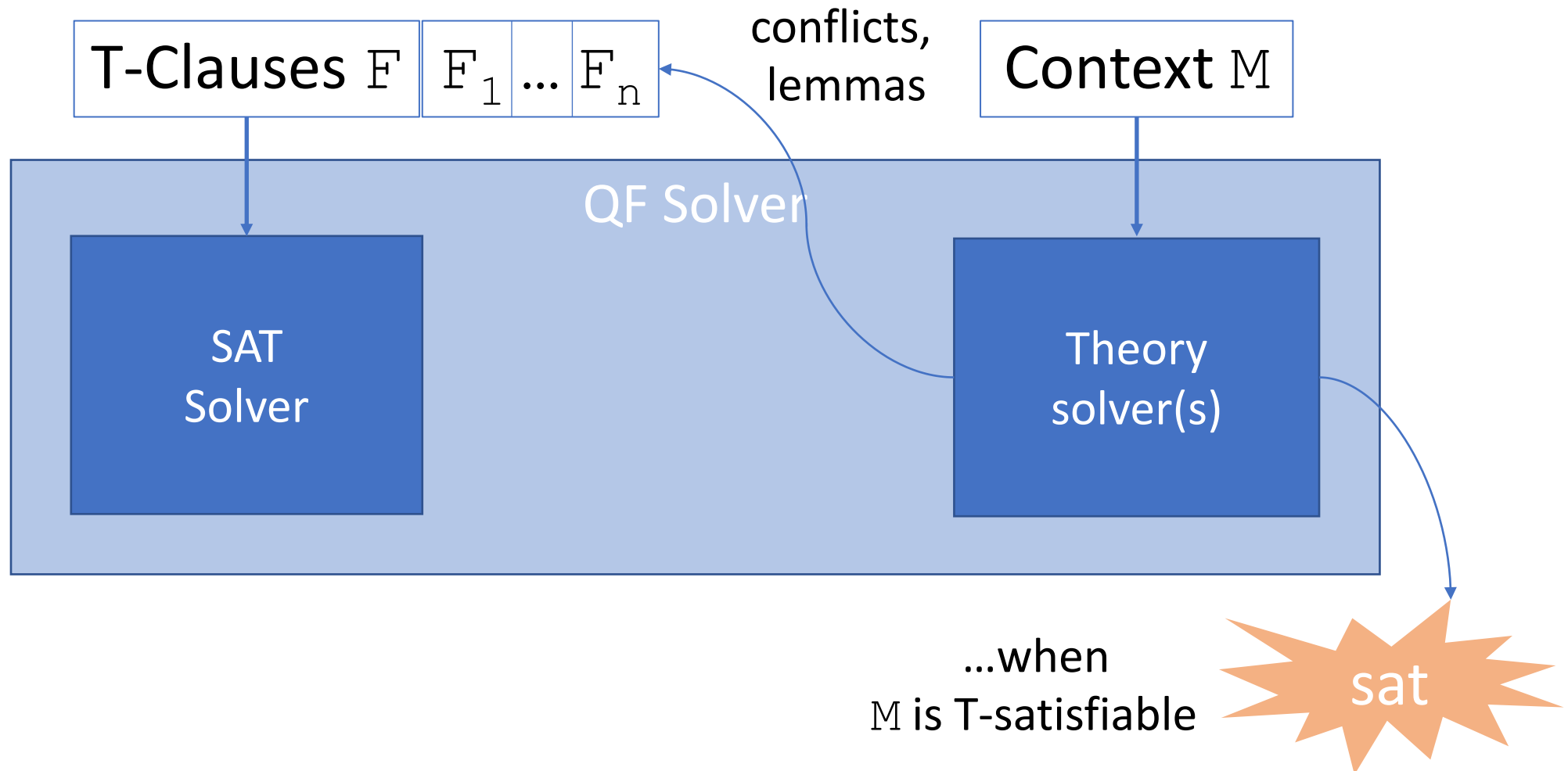


# DPLL(T)-Based SMT Solvers

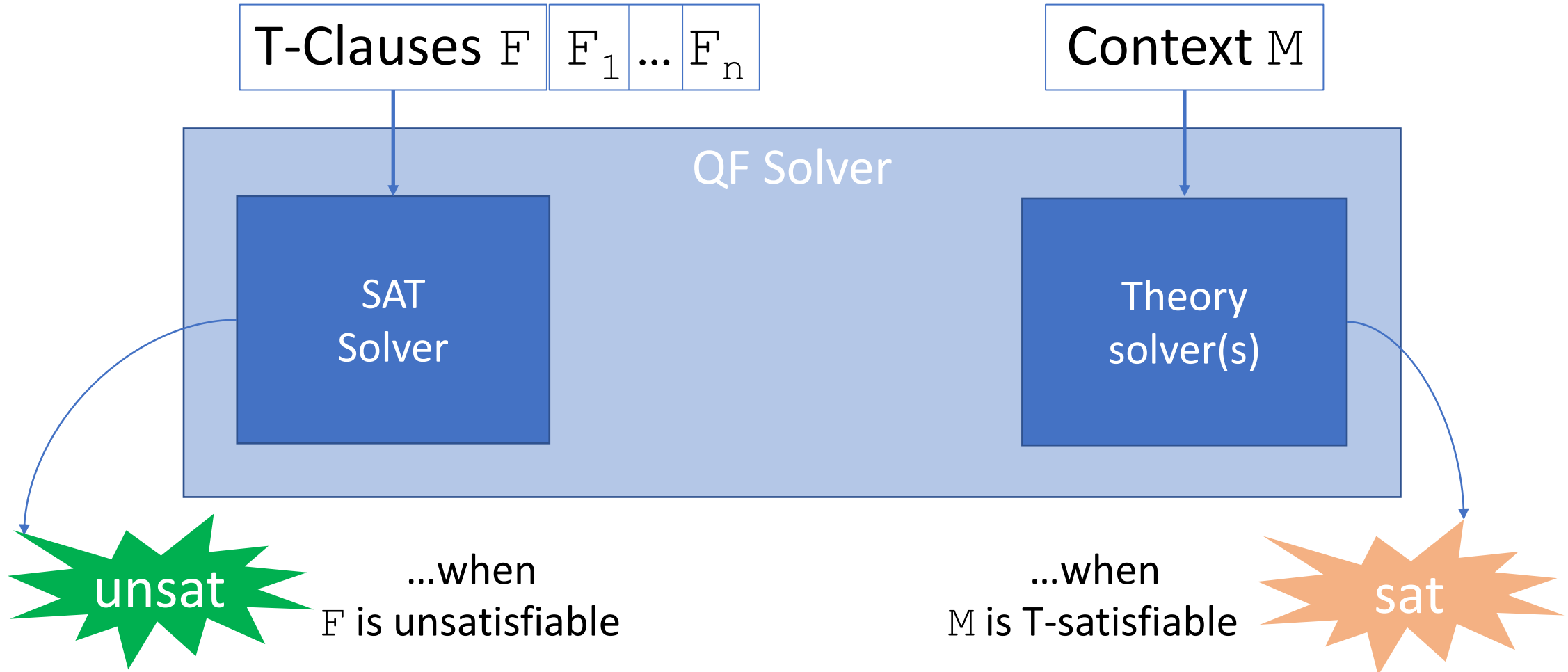




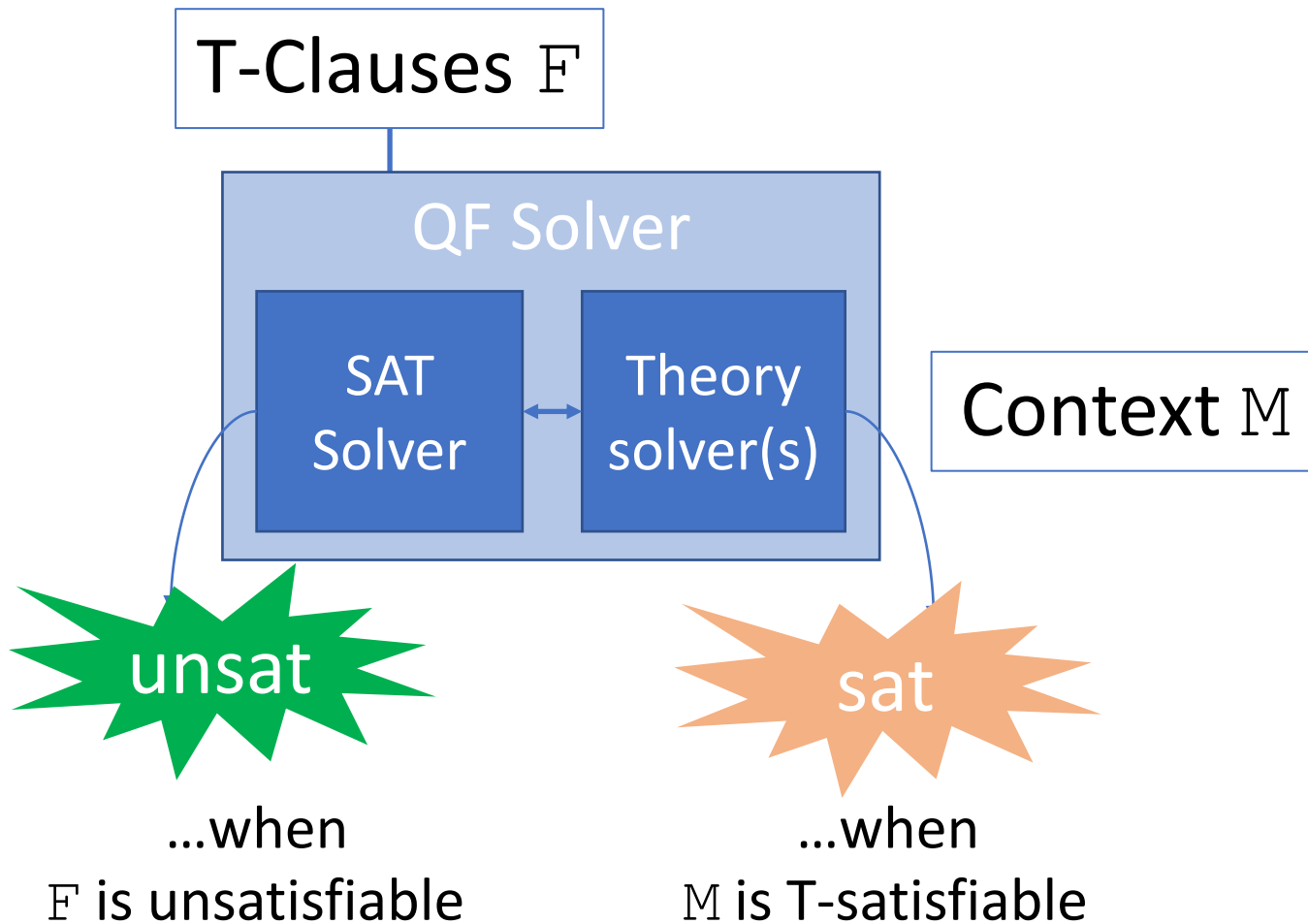
# DPLL(T)-Based SMT Solvers



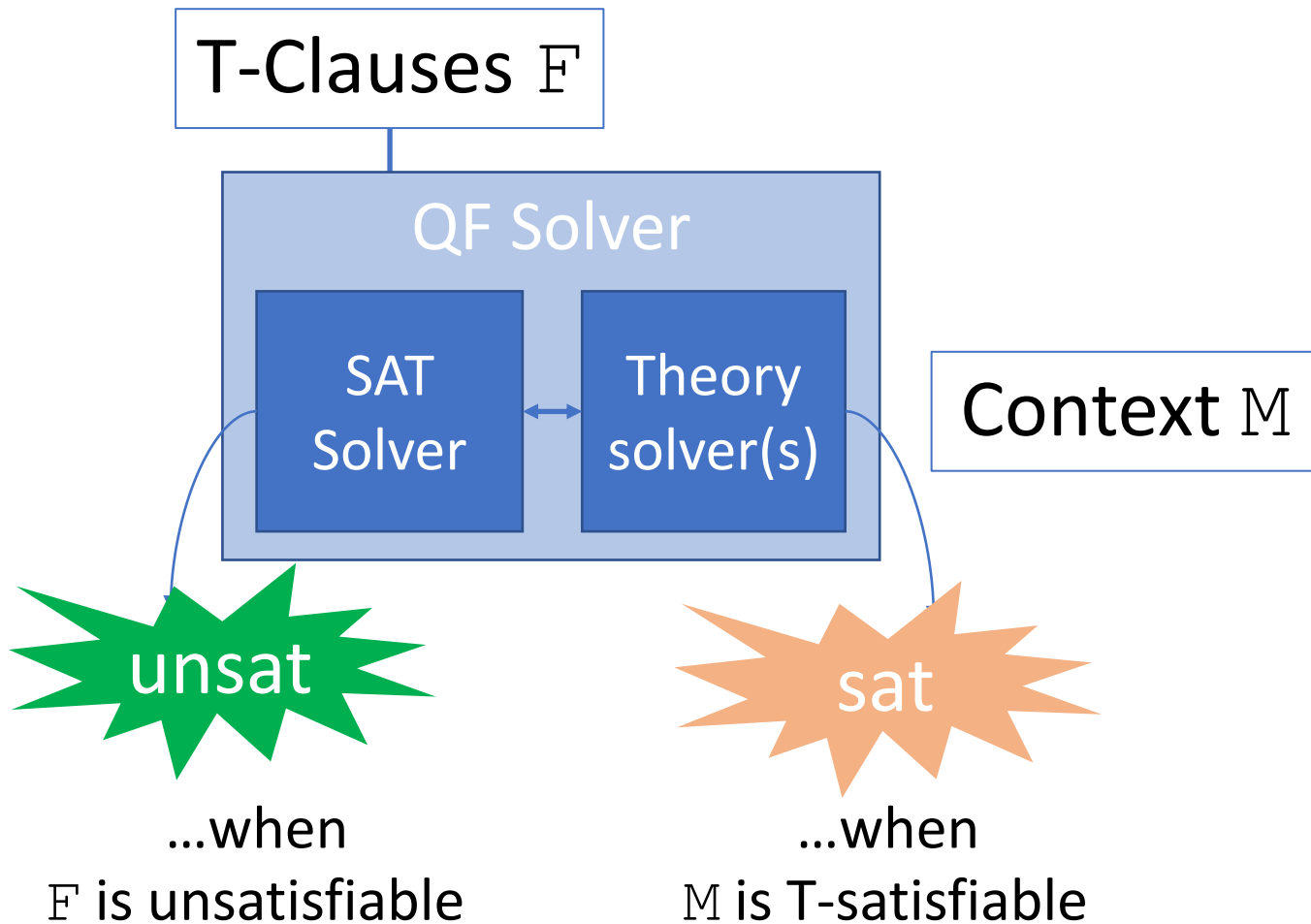
# DPLL(T)-Based SMT Solvers



# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation

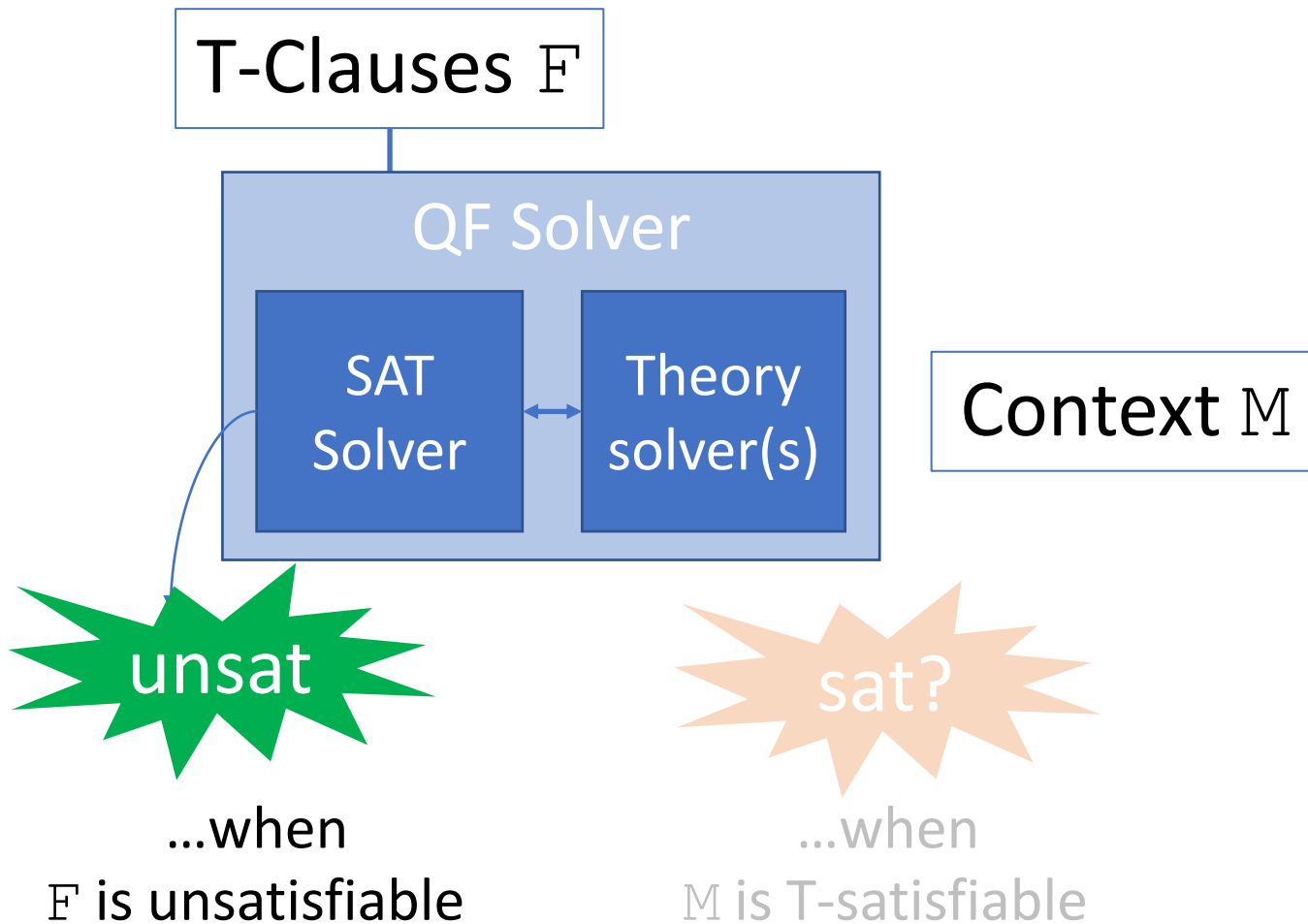


# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



When  $\mathbb{M}$  contains  
quantified formulas  $\forall \dots$

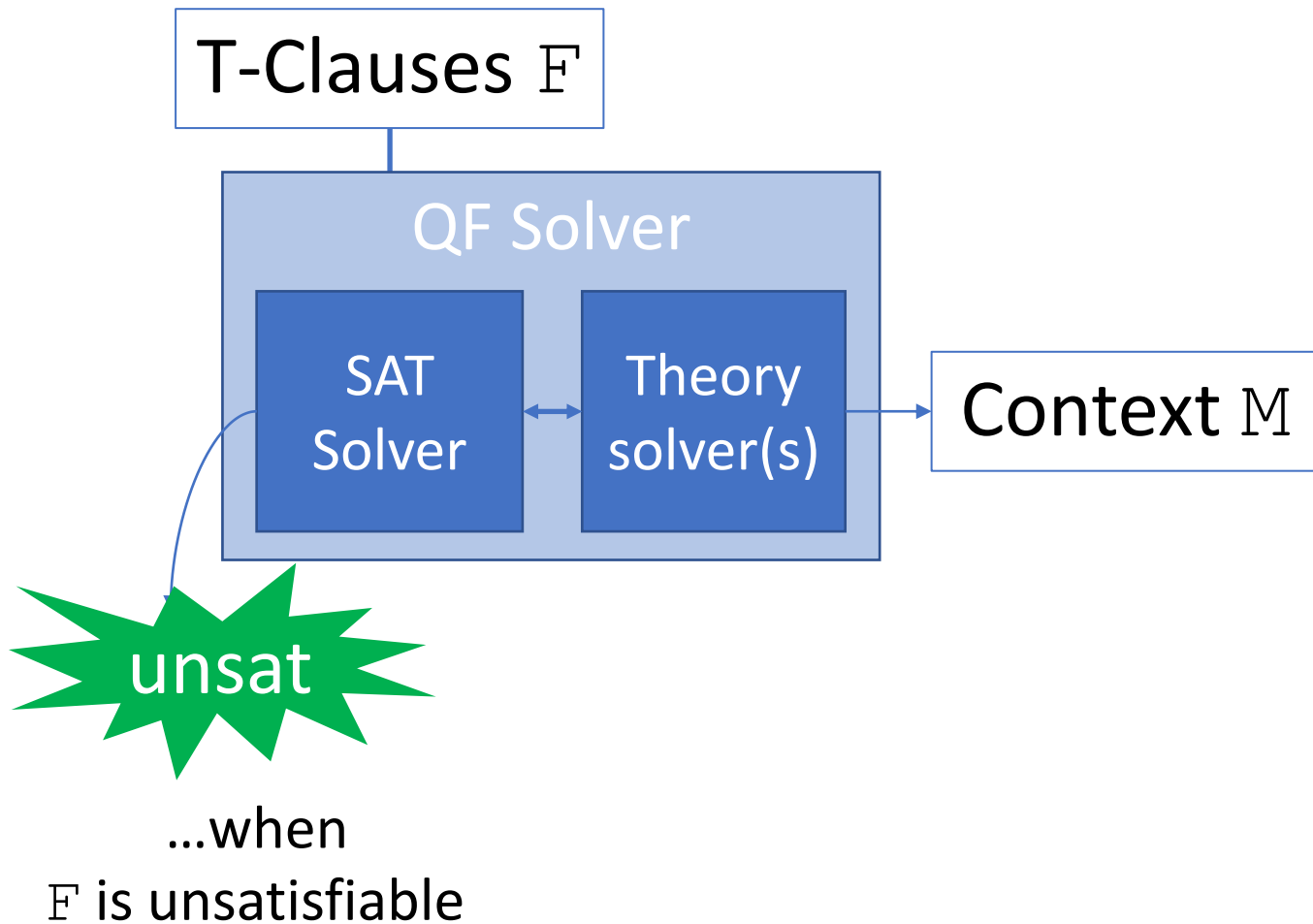
# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



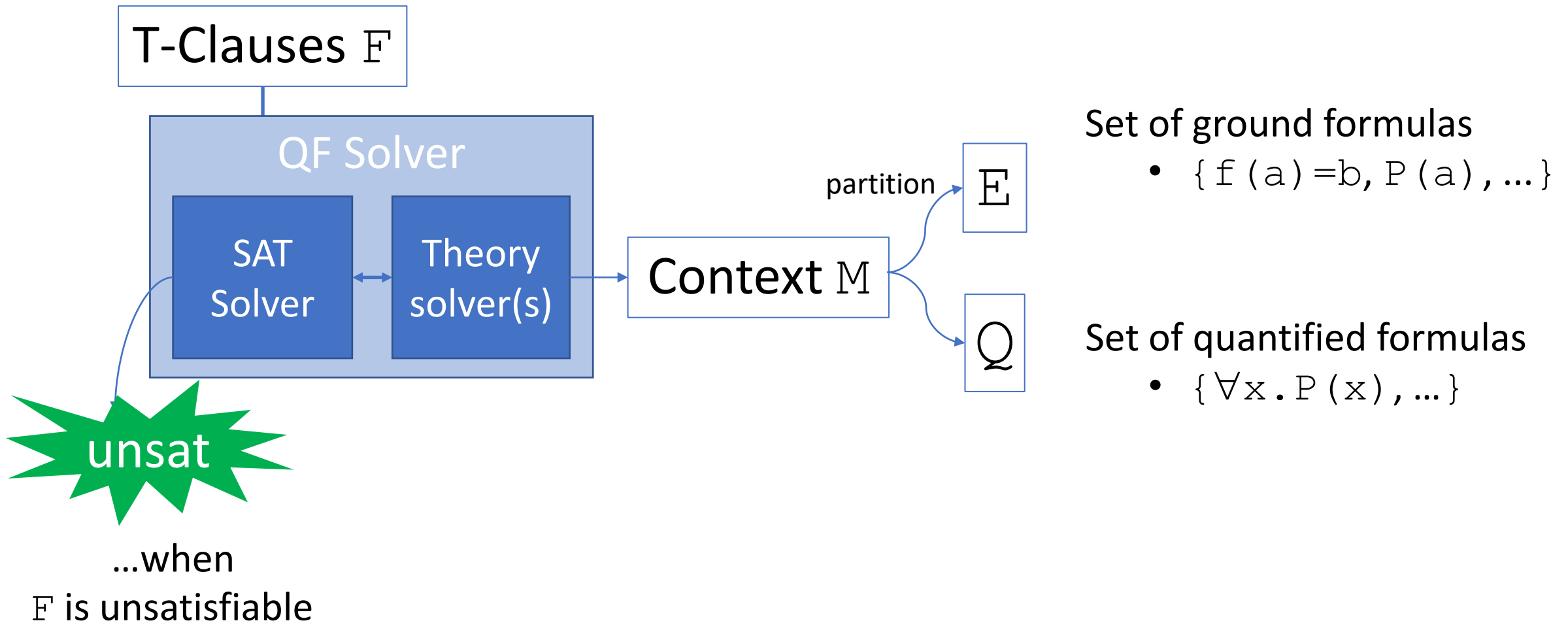
**Undecidability!**

...cannot always establish  $\mathbb{M}$  is sat

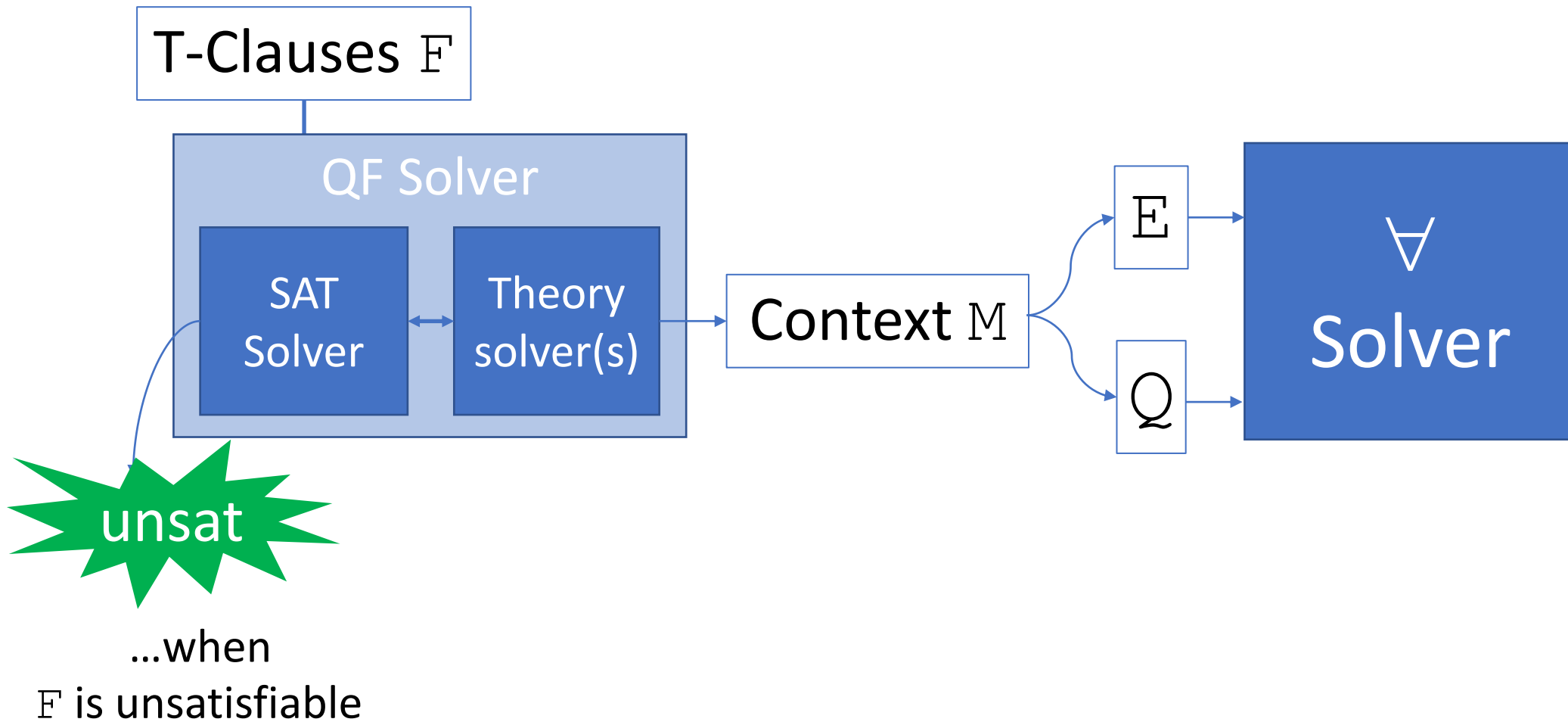
# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation

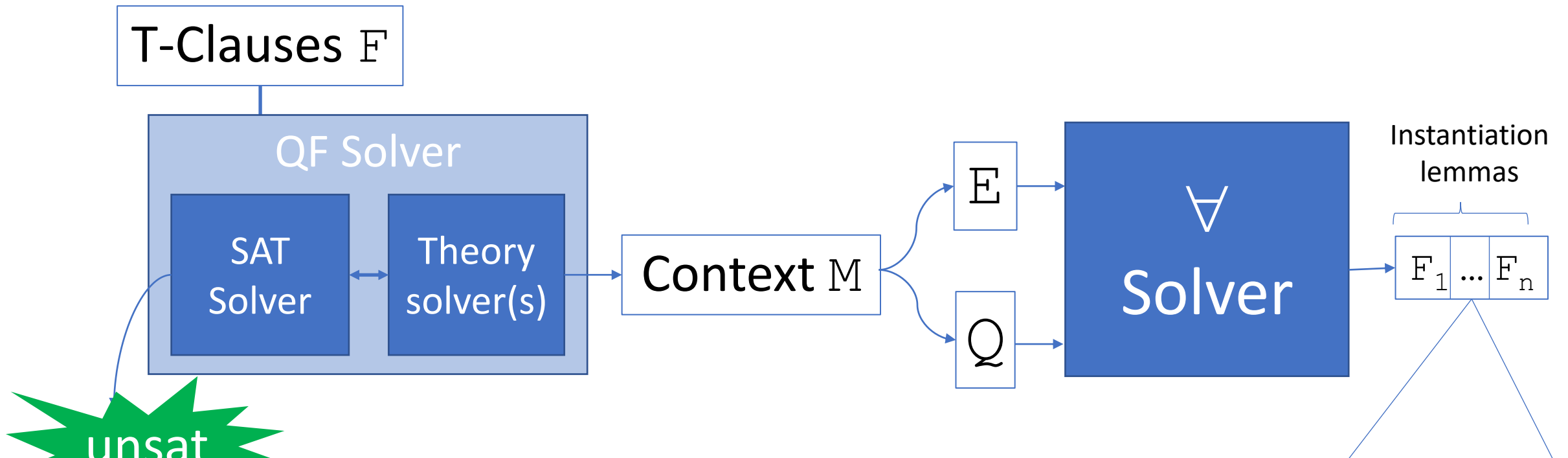


# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation





# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



unsat

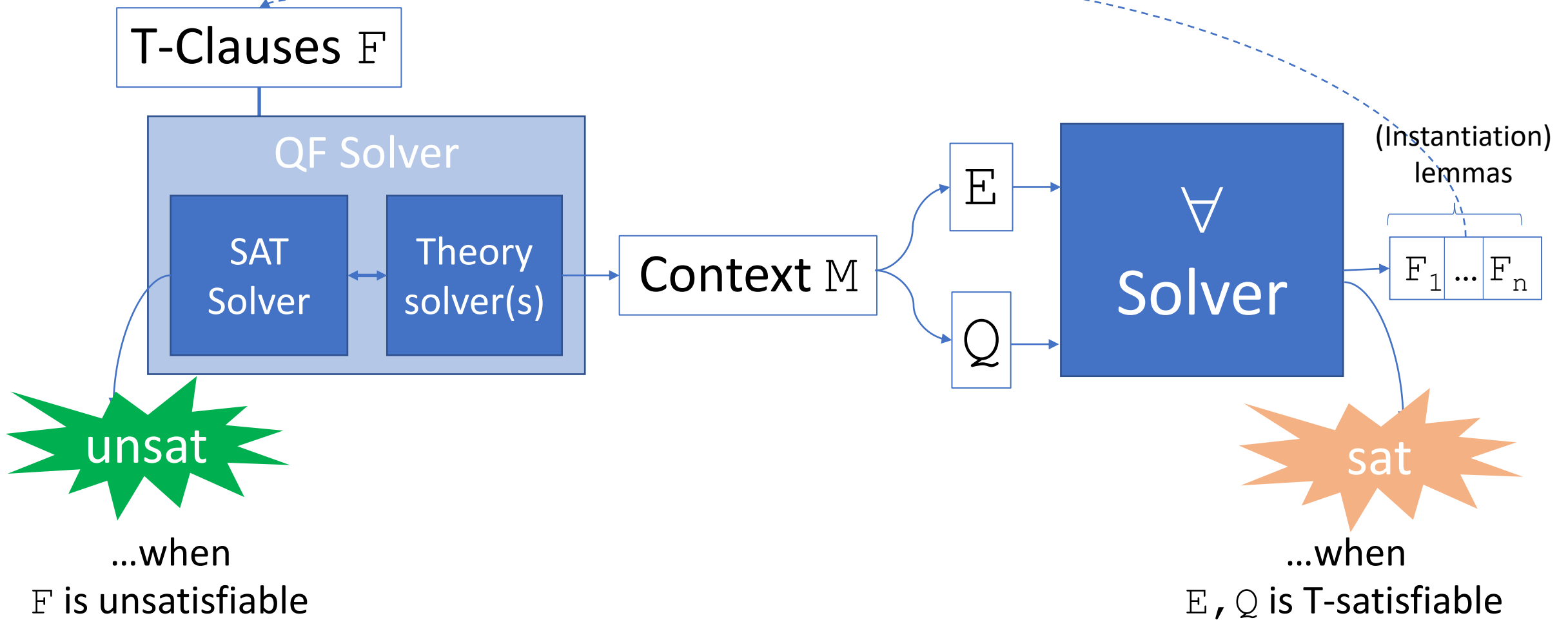
...when  
 $F$  is unsatisfiable

Given  $\forall x . P(x)$  in  $Q$ , *instantiation lemma*  $F_i$  is a valid formula:

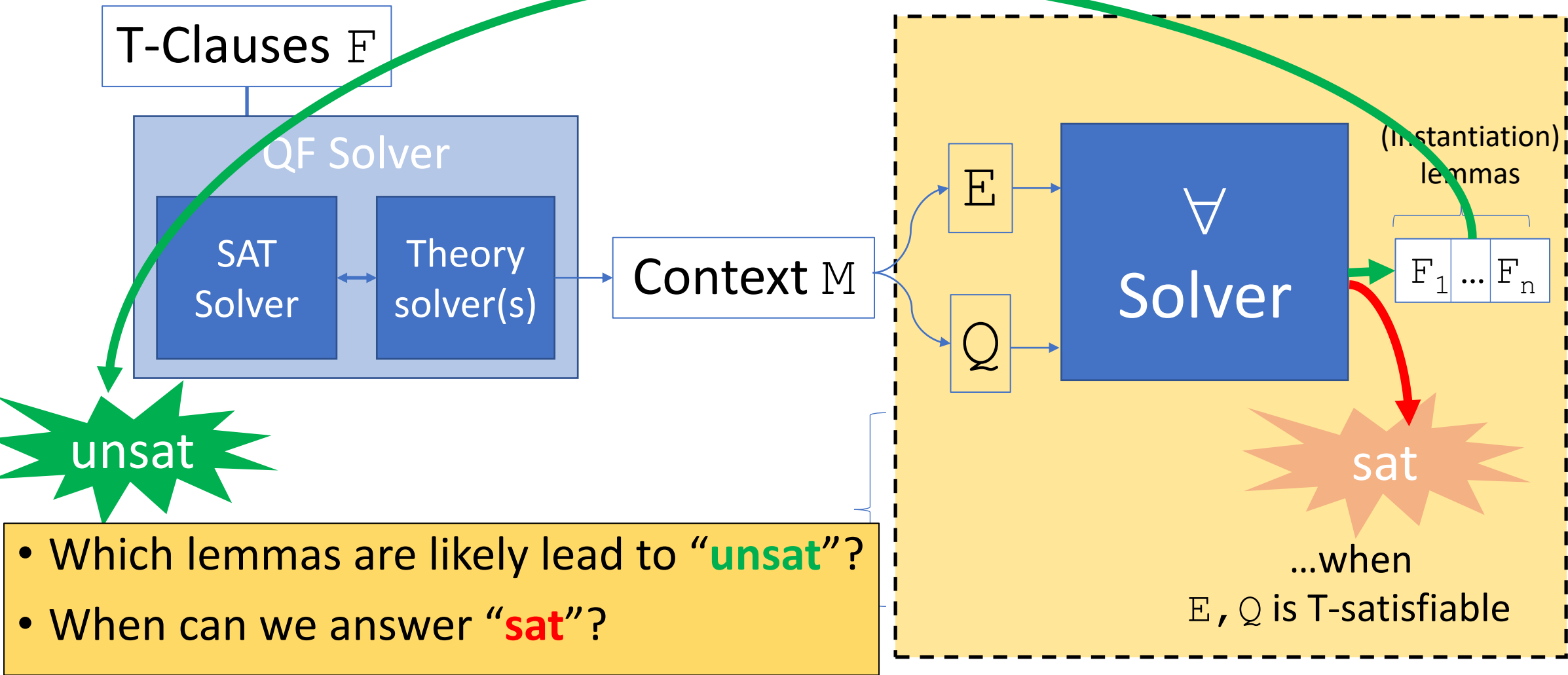
$$\forall x . P(x) \Rightarrow P(t)$$

for some ground term  $t$

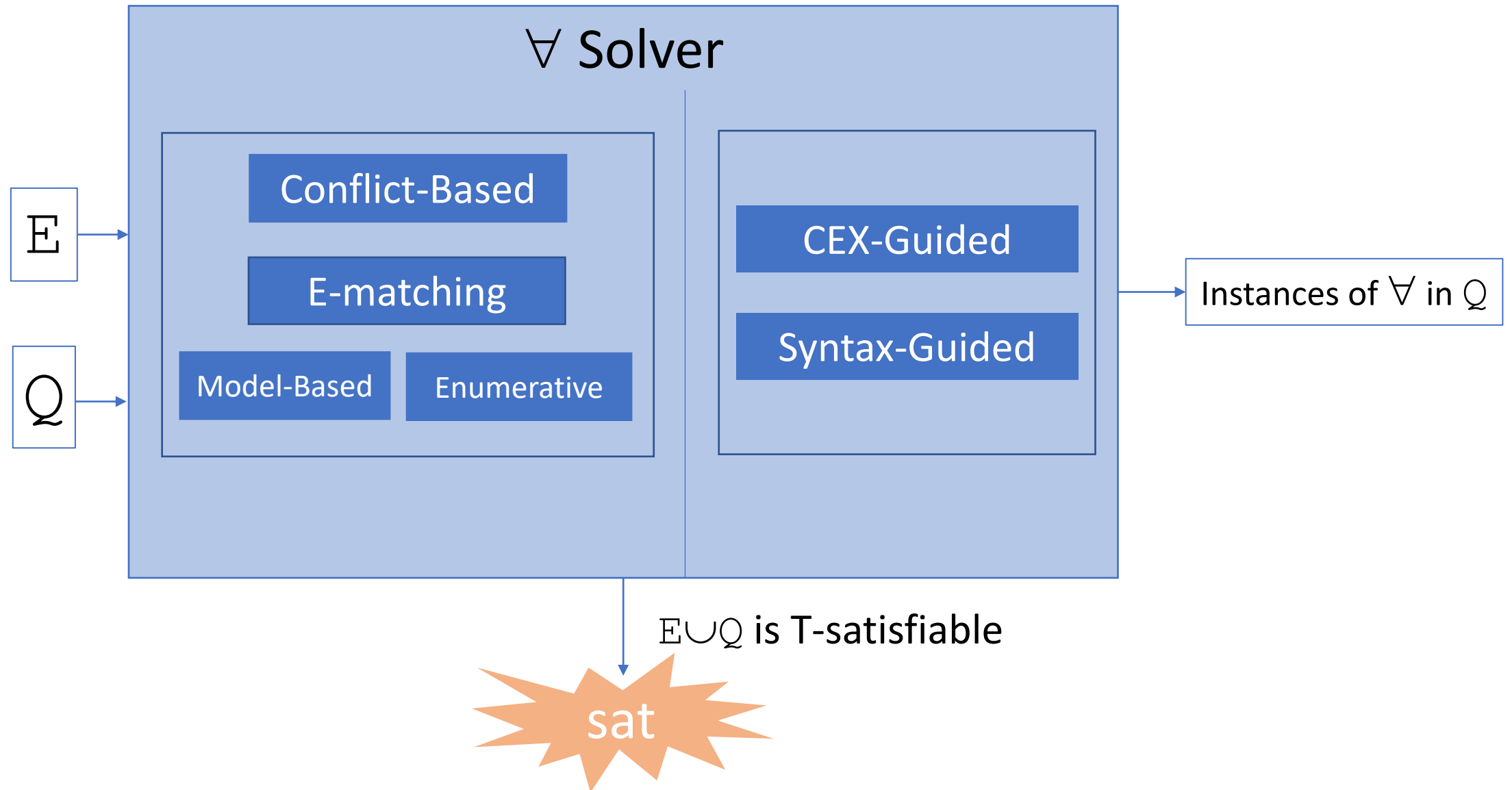
# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



# DPLL(T)-Based SMT Solvers + $\forall$ Instantiation



# Techniques for Quantifier Instantiation



## ∇ Solver

Conflict-Based

E-matching

Model-Based

Enumerative

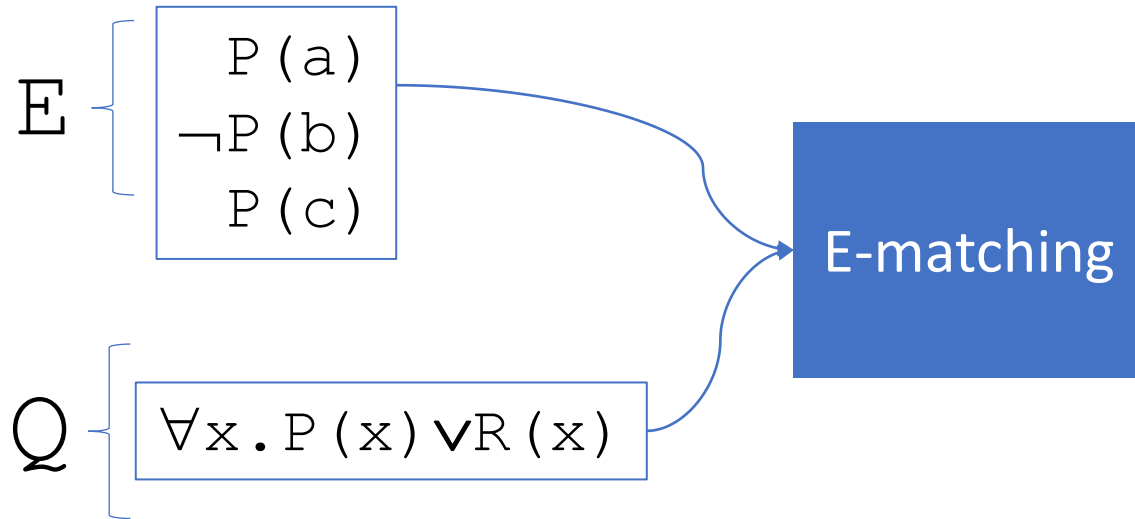
CEX-Guided

Syntax-Guided

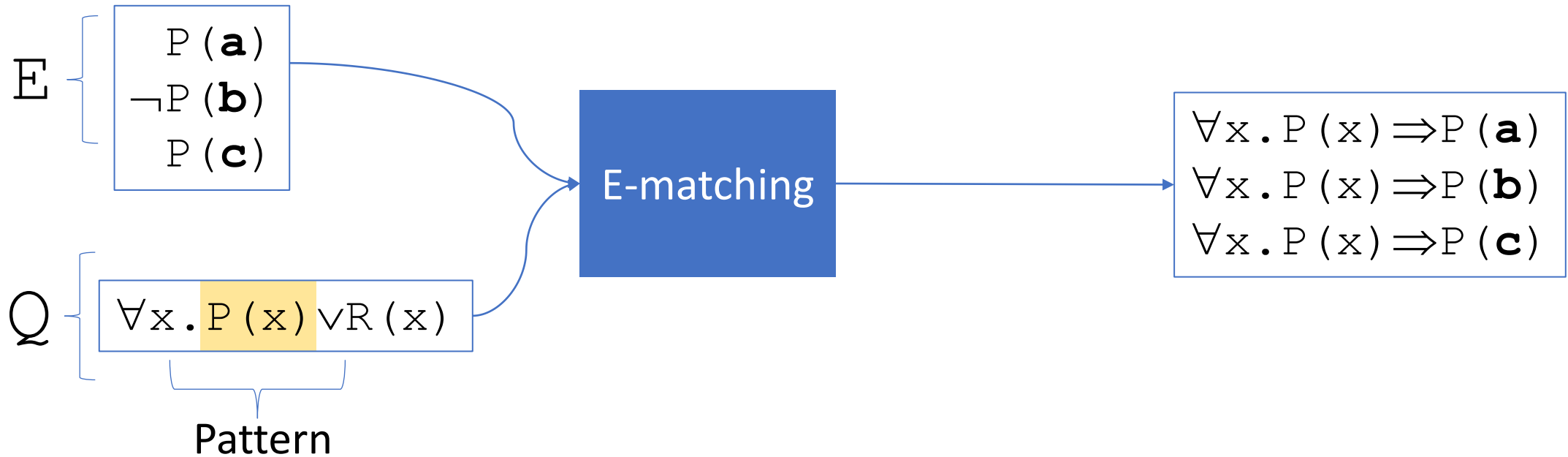
# E-matching

- **Idea:** Instantiations found by pattern matching  $Q$  to terms from  $\mathbb{E}$
- Implemented in early SMT solvers (e.g. simplify) as well as z3, cvc5
  - [Detlefs et al 2005, Bjorner et al CADE 2007, Ge et al CAV 2007]
- **Key applications:** Software verification
  - Example: Dafny/Boogie

# E-matching



# E-matching





# E-matching: Impact

- Highly effective for quantifiers with UF
  - Widely used as backend for many software verification applications
- Challenges:
  - Pattern selection, multi-patterns
  - ***Too many*** instances produced, non-termination (matching loops)
    - ...solver times out
  - ***Incomplete***
    - ...solver answers “unknown”

## ∇ Solver

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E-matching

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Enumerative

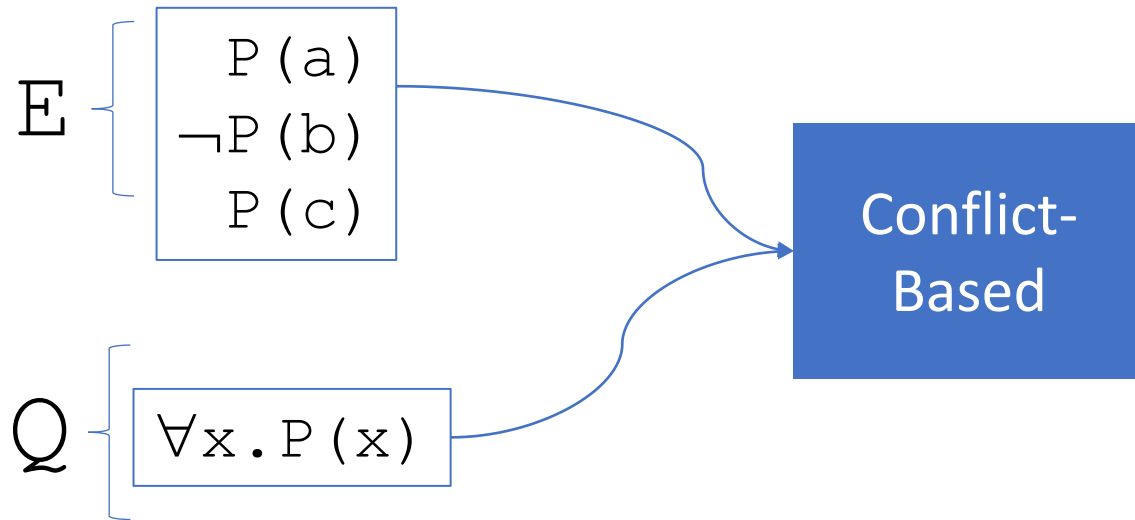
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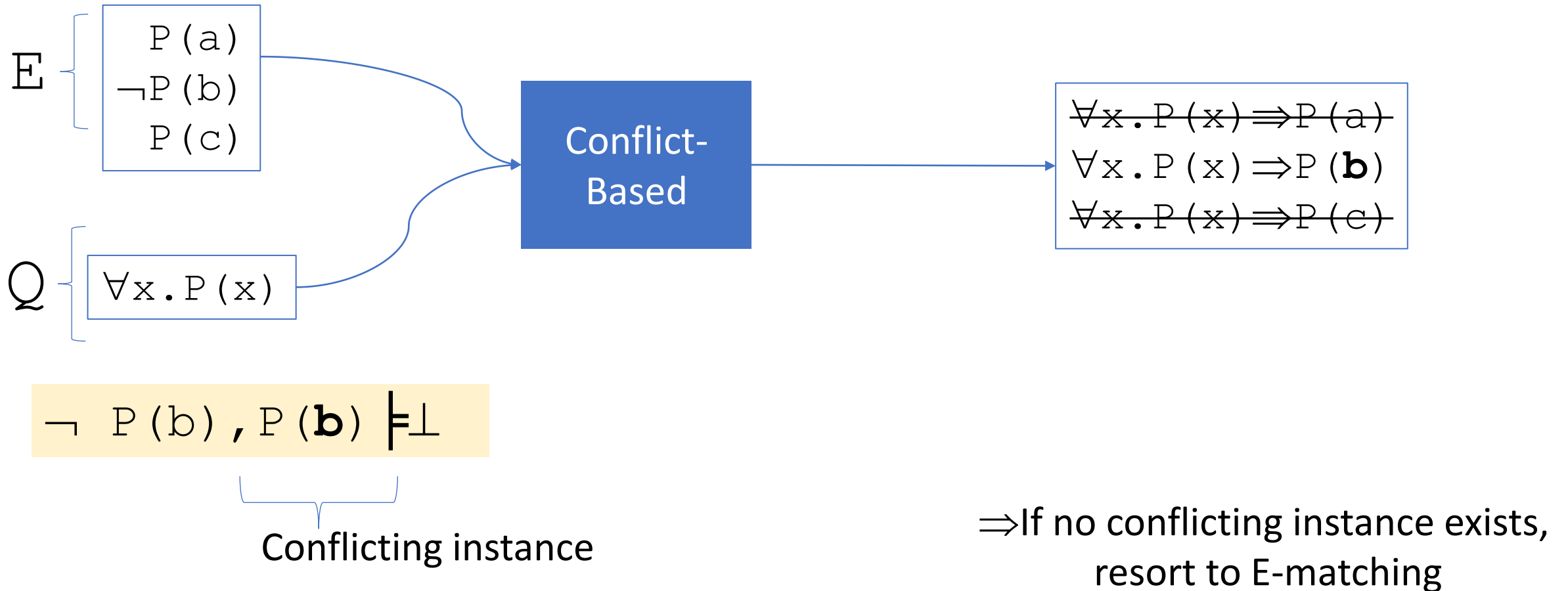
# Conflict-Based Instantiation

- **Idea:** Find instantiation that is in conflict with  $\exists$ , if it exists
- A *conflicting instance* forces the solver to backtrack
  - Improves ability to answer “unsat”
- Implemented in cvc5, veriT
  - [\[Reynolds et al FMCAD 2014, Barbosa et al TACAS 2017\]](#)
- **Key applications:** Automated Theorem Proving
  - Example: Isabelle/Sledgehammer

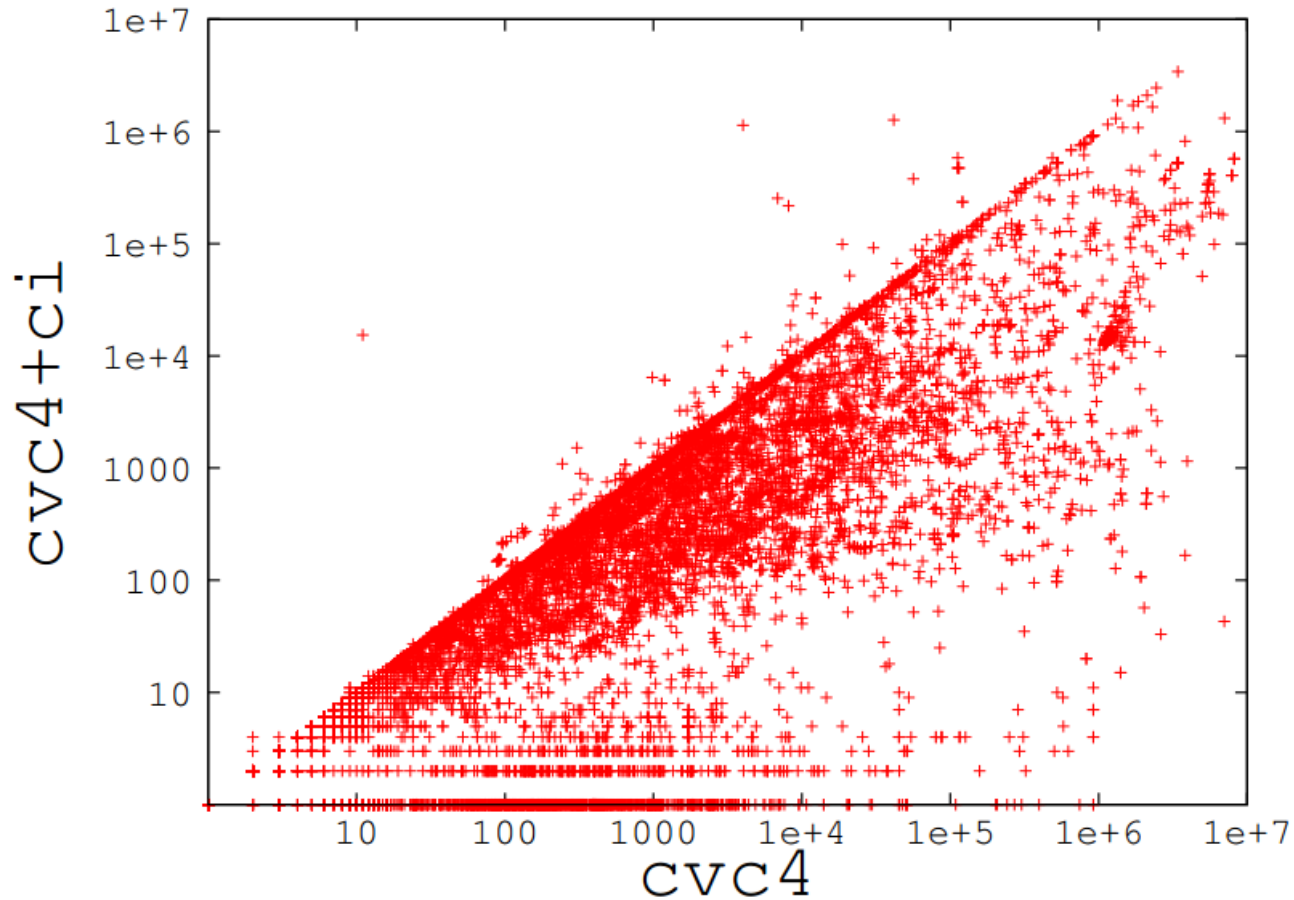
# Conflict-Based Instantiation



# Conflict-Based Instantiation



# Conflict-Based Instantiation: Impact



Reported number of instances.

- Using conflict-based instantiation (`cvc4+ci`), require an order of magnitude fewer instances for showing “UNSAT” wrt E-matching alone

(taken from [\[Reynolds et al FMCAD14\]](#), evaluation On SMTLIB, TPTP, Isabelle benchmarks)

# Conflict-Based Instantiation: Impact

- CVC4 with conflicting instances **cvc4+ci**
  - Solves the **most benchmarks** for TPTP and Isabelle
  - Requires almost an order of magnitude **fewer instantiations**

	TPTP		Isabelle		SMT-LIB	
	Solved	Inst	Solved	Inst	Solved	Inst
<b>cvc3</b>	5,245	627.0M	3,827	186.9M	3,407	42.3M
<b>z3</b>	6,269	613.5M	3,506	67.0M	<b>3,983</b>	6.4M
<b>cvc4</b>	6,100	879.0M	3,858	119.0M	3,680	60.7M
<b>cvc4+ci</b>	<b>6,616</b>	150.9M	<b>4,082</b>	<b>28.2M</b>	3,747	32.4M

⇒ A number of hard benchmarks can be solved without resorting to E-matching at all

## ∇ Solver

Conflict-Based

E-matching

Model-Based

Enumerative

CEX-Guided

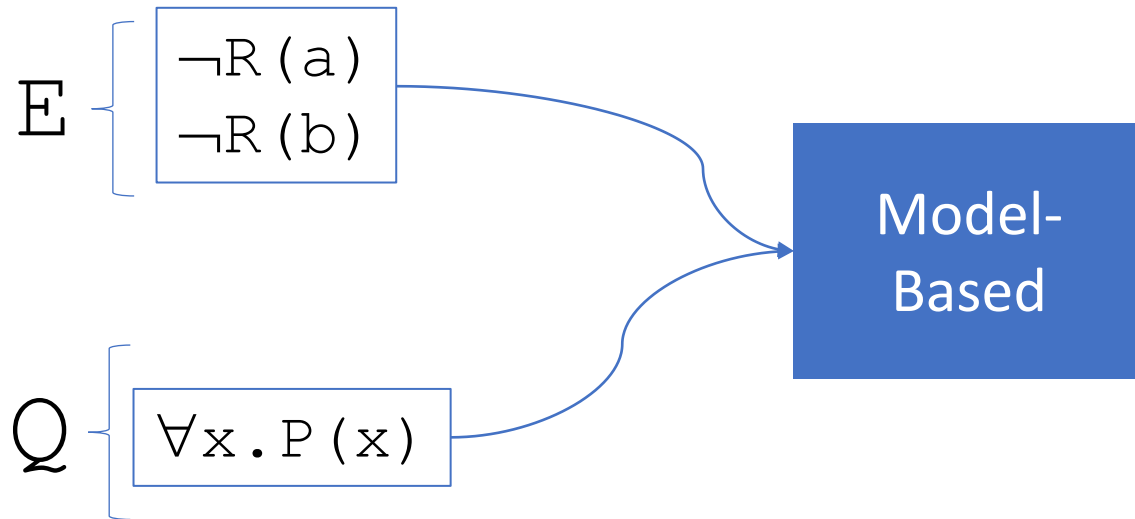
Syntax-Guided



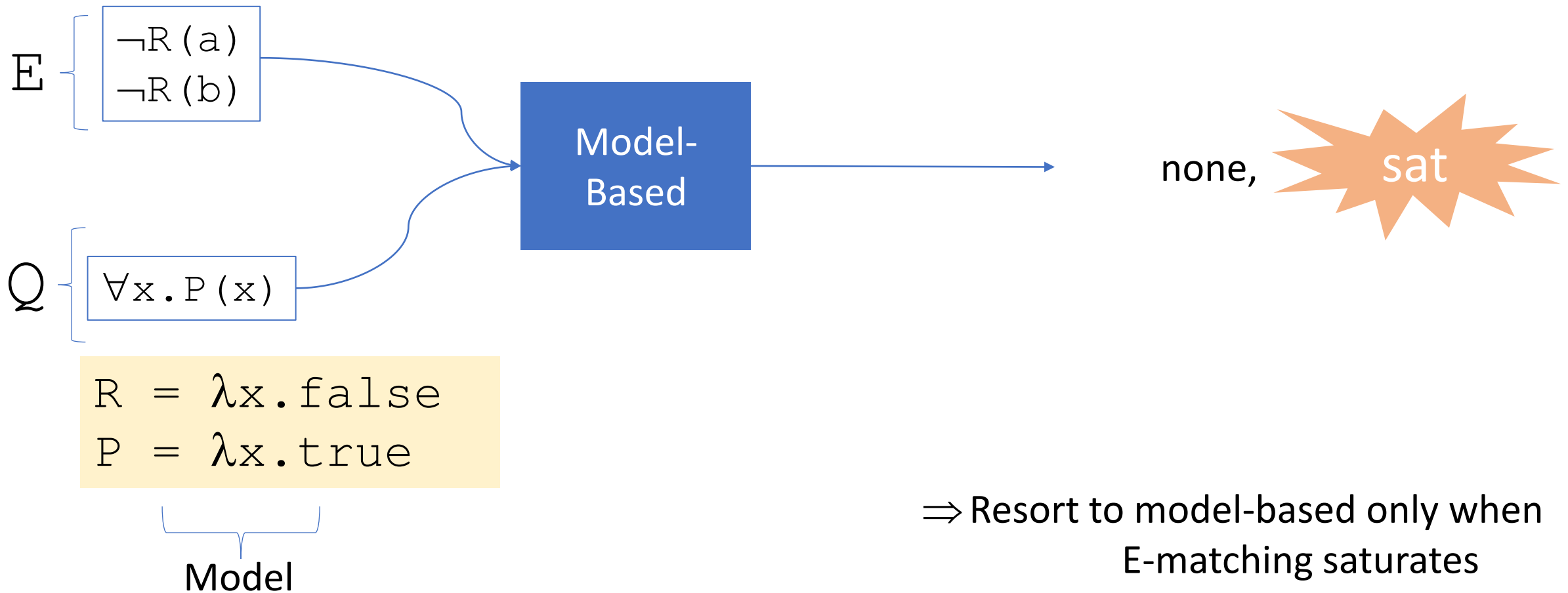
# Model-Based Instantiation

- **Idea:** Instantiate quantifiers based on (complete) models for  $\exists$
- Complete for certain fragments, e.g. EPR, essentially uninterpreted
  - Can be useful for answering “sat”
- Implemented in z3, finite model finding in cvc4
  - [\[Ge et al 2009, Reynolds et al 2013\]](#)
- **Key applications:** Software Design, Planning
  - Example: Alloy Analyzer

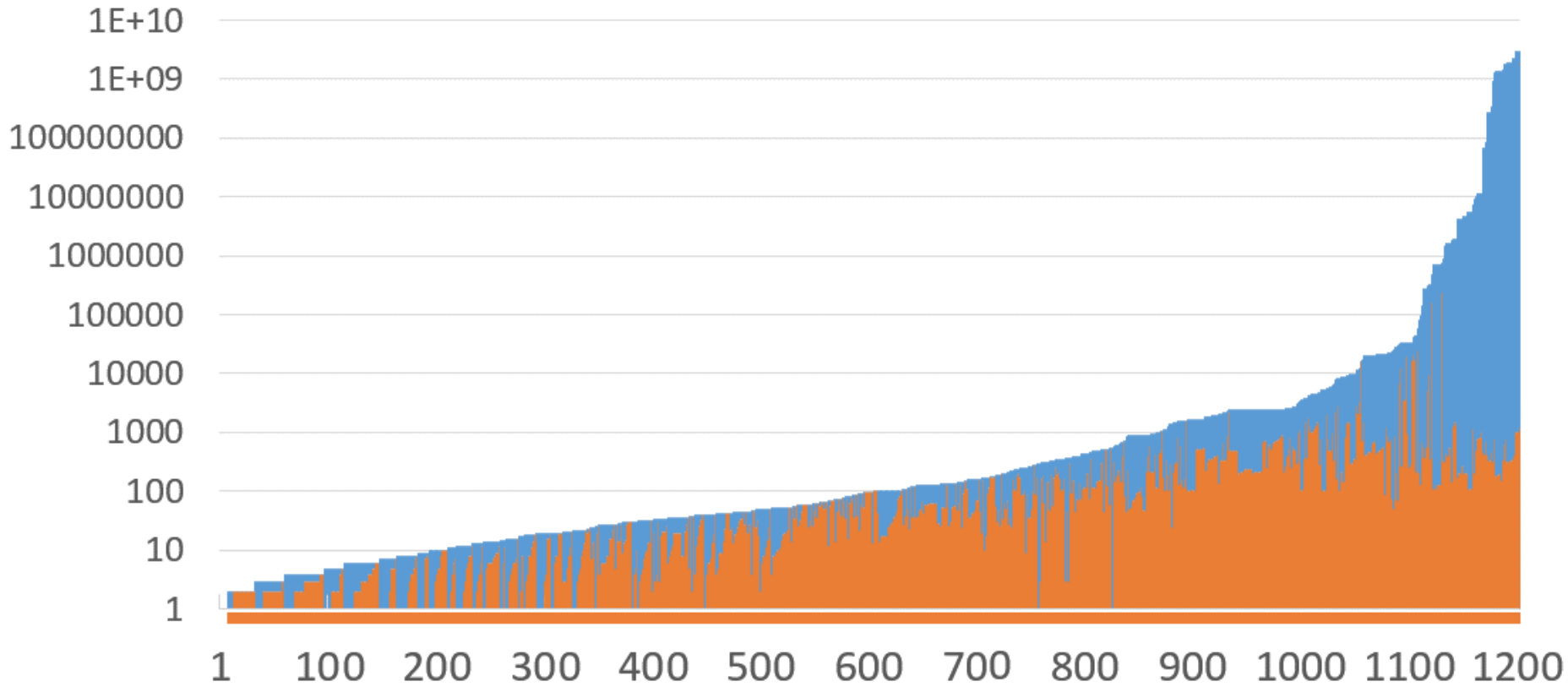
# Model-Based Instantiation



# Model-Based Instantiation



# Model-based Instantiation: Impact



- CVC4 Finite Model Finding + Model-Based instantiation [[Reynolds et al CADE13](#)]
  - Approach can scale to domains of >2 billion, only adds fraction of possible instances

## ∇ Solver

Conflict-Based

E-matching

Model-Based

Enumerative

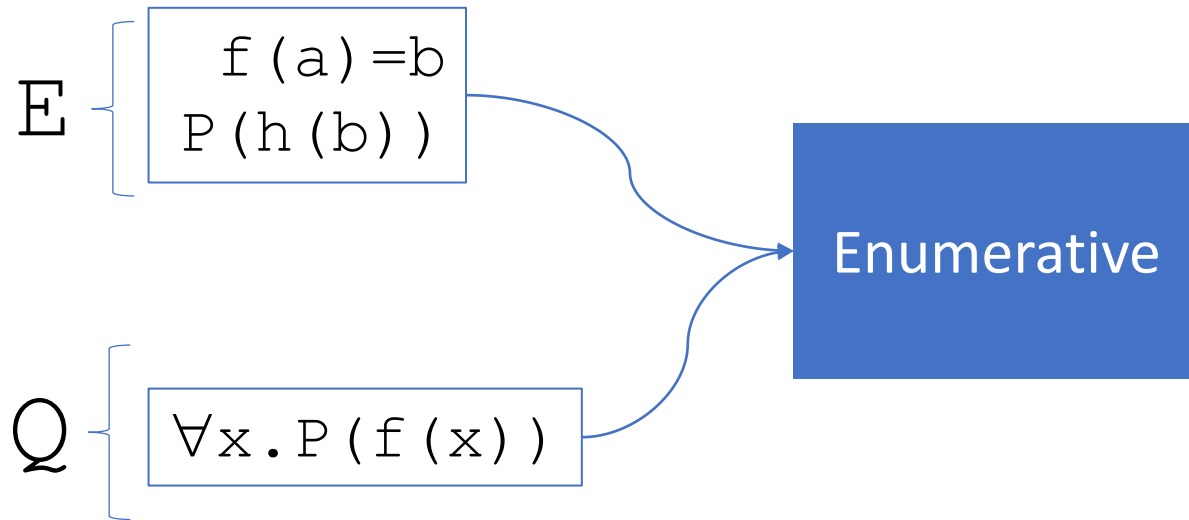
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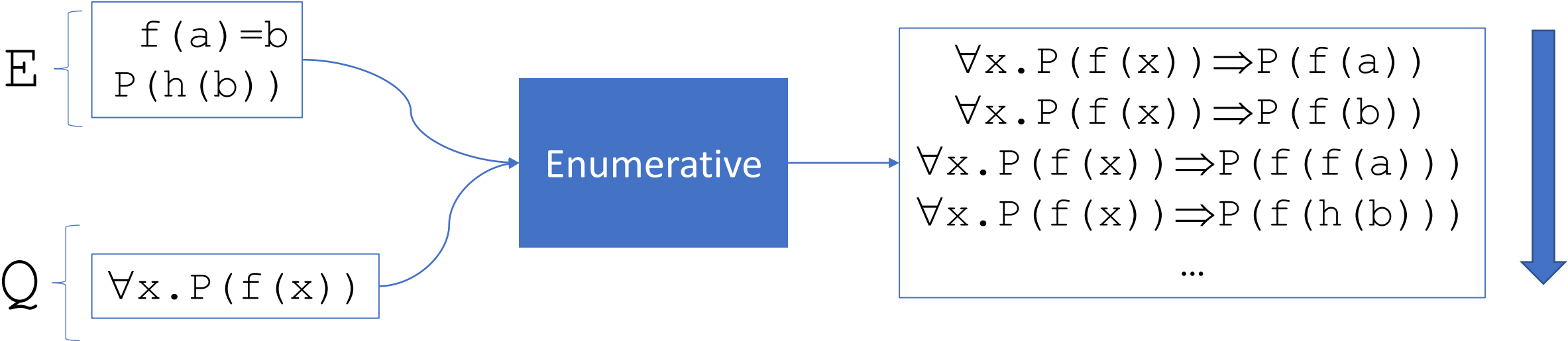
# Enumerative Instantiation

- **Idea:** Instantiate based on (fair) enumeration of terms from  $\mathbb{E}$
- Effective alternative to model-based, better performance for “unsat”
- Complete for limited fragments
- Implemented in cvc5, veriT
  - [\[Reynolds et al TACAS 2018, Janota et al 2021\]](#)
- Key applications: Automated theorem proving
  - Example: Isabelle/Sledgehammer, TPTP

# Enumerative Instantiation



# Enumerative Instantiation



$a < b < f(a) < h(b) < \dots$

Ordering over terms from  $\mathbb{E}$

$\Rightarrow$  Finds instances that E-matching may miss, more lightweight than MBQI



# $\forall$ Solver

Conflict-Based

E-matching

Model-Based

Enumerative

Generally,  
used for  $\forall$  with UF logics

CEX-Guided

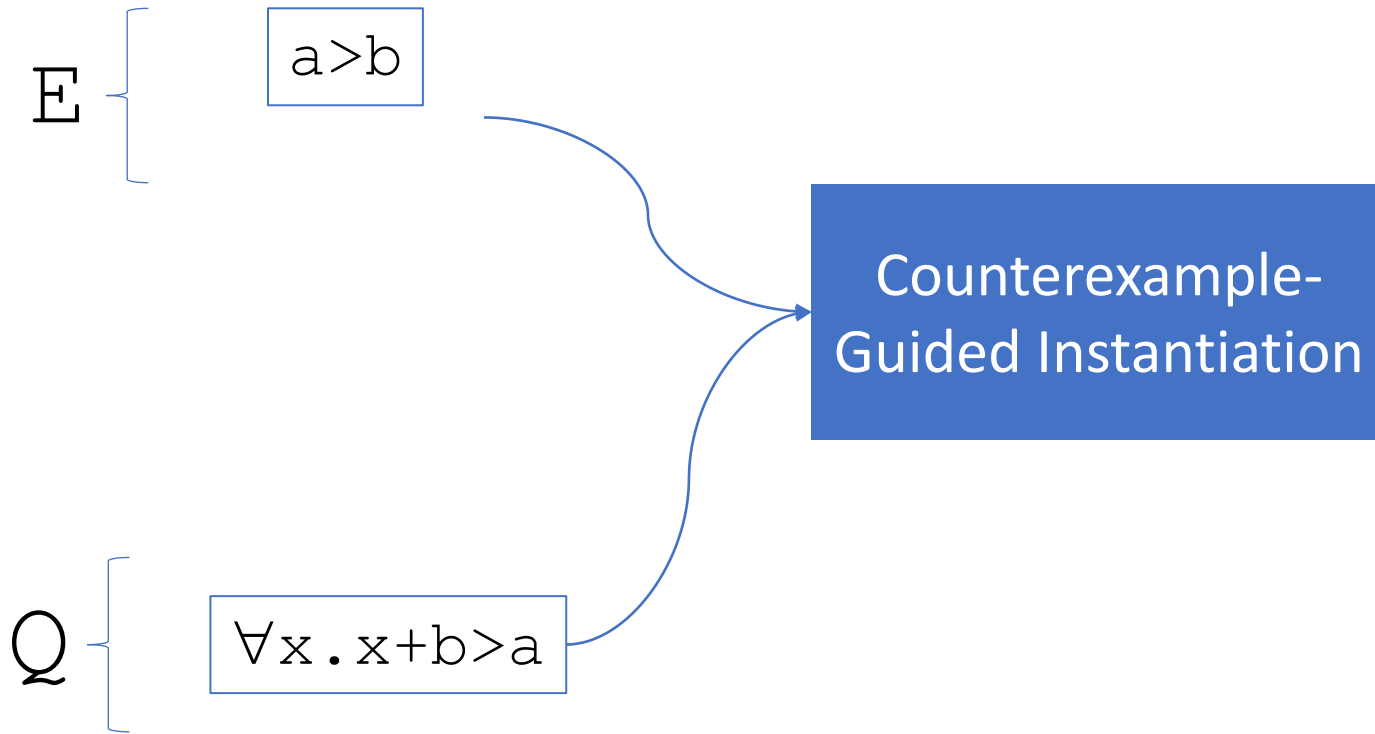
Syntax-Guided

Generally,  
used for  $\forall$  in **pure theories**

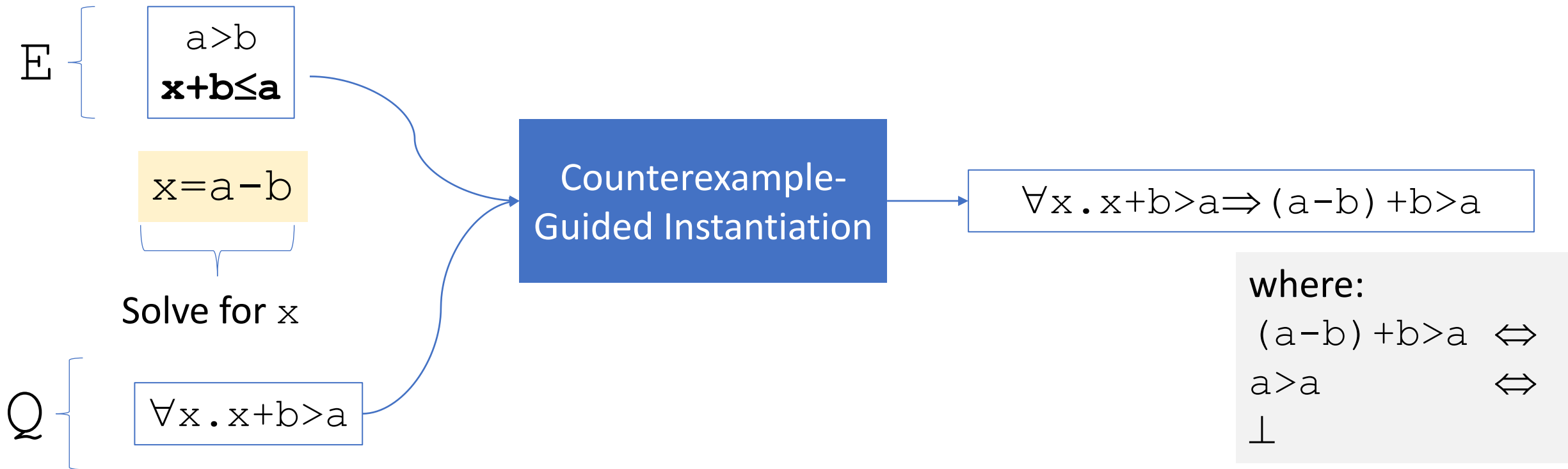
# Counterexample-Guided Instantiation

- **Idea:** Instantiate based on T-solving for counterexamples  $\neg Q \wedge E$
- Can be seen as a lazy quantifier elimination algorithm in SMT loop
- Complete for quantified linear integer/real arithmetic, finite domains
- Variants of idea implemented in cvc5, (extensions of) z3, yices
  - [Bjorner 2012, Komuravelli et al 2014, Dutertre 2015, Reynolds 2015, Bjorner/Janota 2016, Fediyukovich et al 2016]
- Key applications: Synthesis, Hardware Verification, Compiler Optimization

# Counterexample-guided Instantiation



# Counterexample-guided Instantiation



$\Rightarrow$  Can simulate e.g. Cooper, Loos-Weispfenning, Ferrante-Rackoff algorithms for QE

## ∇ Solver

Conflict-Based

E-matching

Model-Based

Enumerative

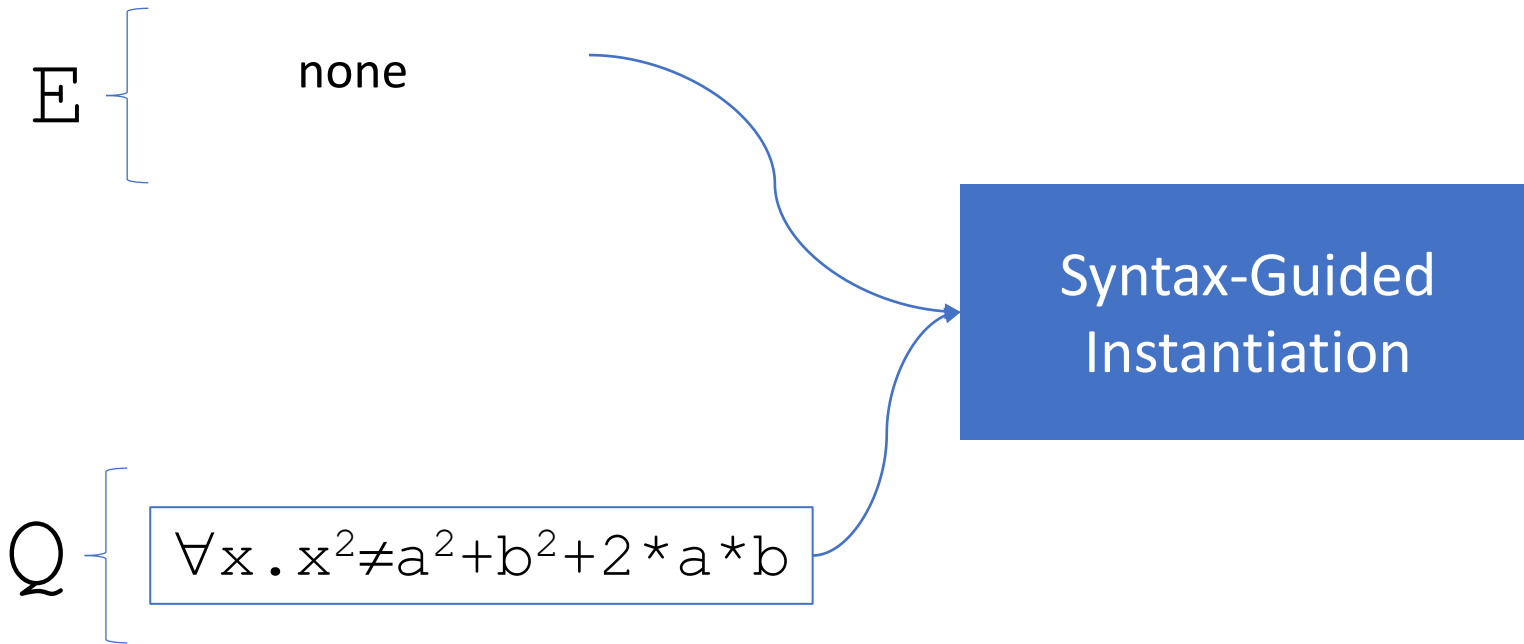
CEX-Guided

Syntax-Guided

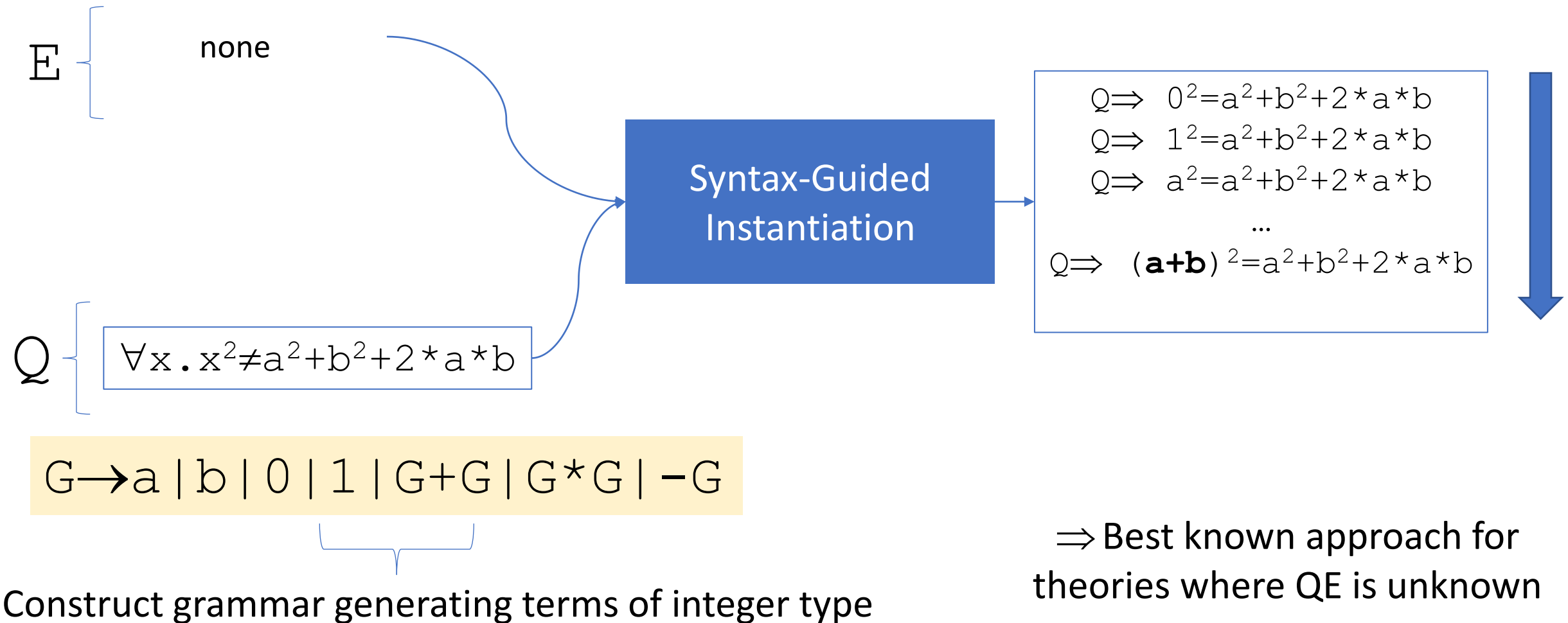
# Syntax-Guided Instantiation

- **Idea:** Instantiate based on enumerating terms from T-specific grammar
- Leverages advances in syntax-guided synthesis (SyGuS) [\[Alur et al 2013\]](#)
- Implemented:
  - For bitvector theory in Boolector [\[Preiner et al TACAS 2017\]](#)
  - For all supported theories in cvc5 [\[Niemetz et al TACAS 2021\]](#)
- Key applications: Synthesis and Verification for emerging theories
  - E.g. quantifiers over floating points

# Syntax-Guided Instantiation



# Syntax-Guided Instantiation





# Summary

- SMT solvers handle diverse set of inputs (with quantifiers)
- Best instantiation technique depends on the logic
  - When UF is present:
    - ⇒ *E-matching, conflict-based, model-based, enumerative*
  - For traditional theories (e.g. LIA, BV) which emit quantifier elimination:
    - ⇒ *Counterexample-guided*
  - For other theories (e.g. floating points, strings, non-linear arithmetic):
    - ⇒ *Syntax-guided*

- Techniques in this talk implemented in SMT solver cvc5
  - Open source
  - Available at : <https://github.com/cvc5>
- ...Thanks for listening!